

FIG. 1A
(Prior Art)

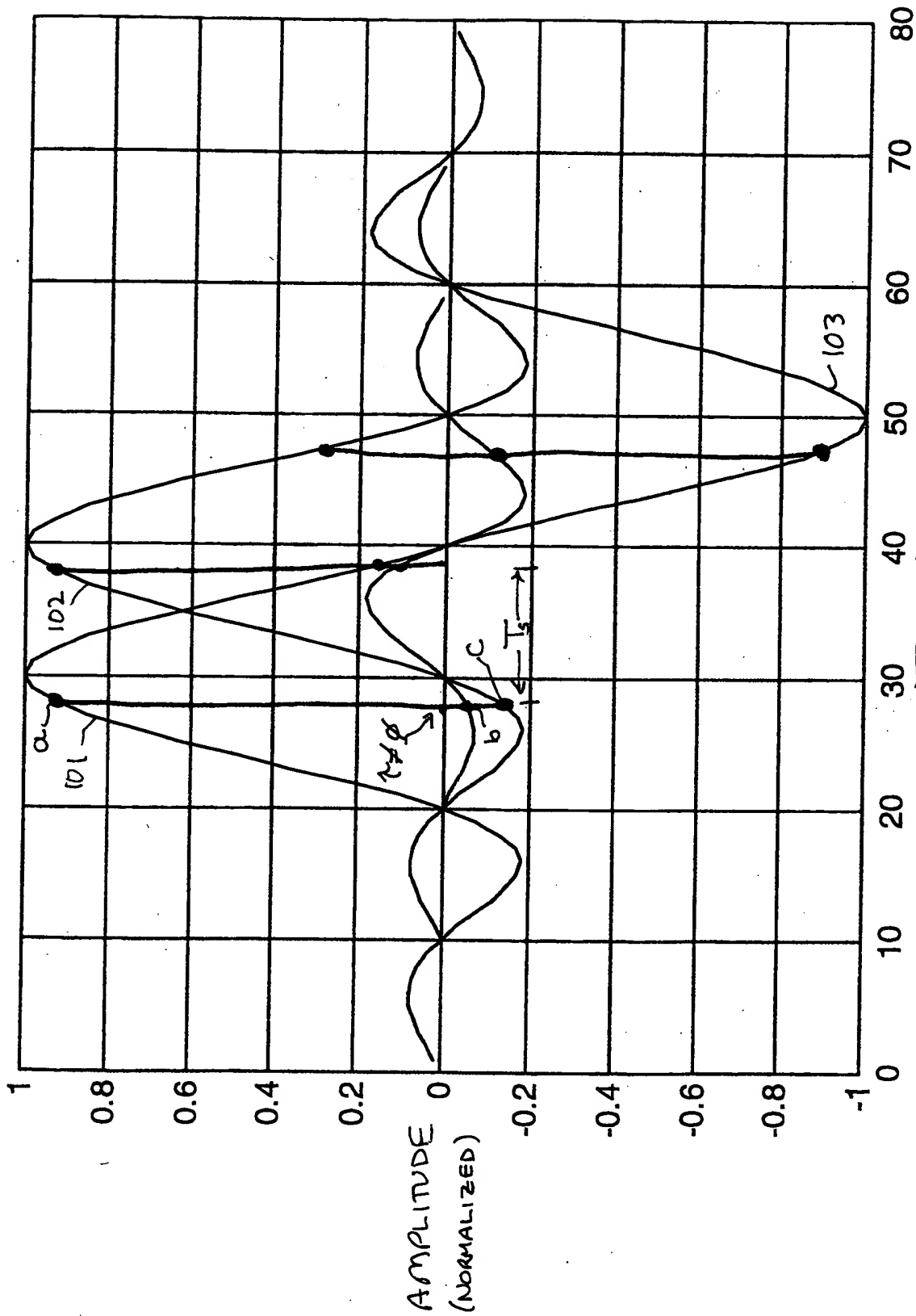


FIG. 1B
(PRIOR ART)

Non ideal sampling yielding ISI. $\tau \neq 0$

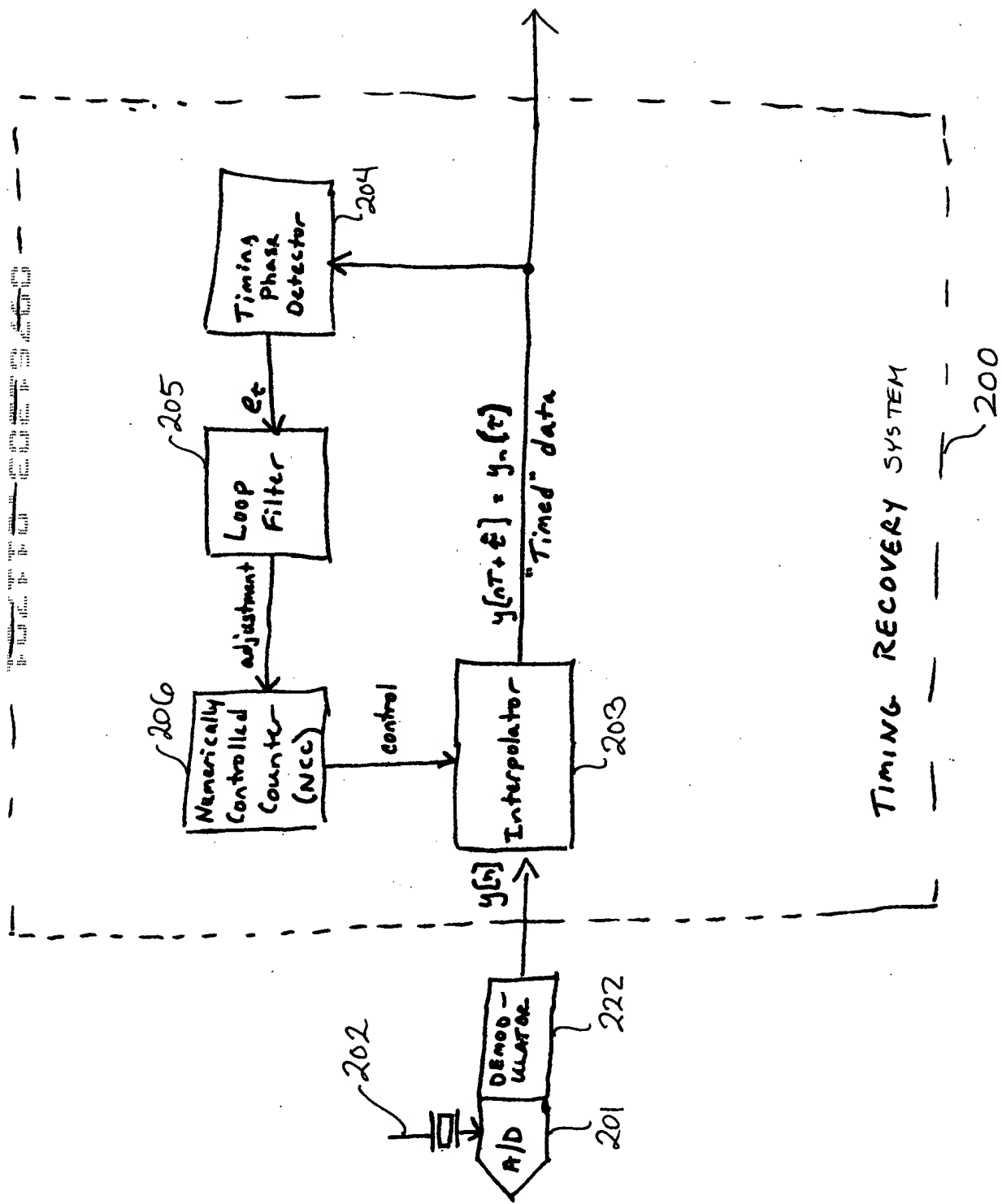


FIG. 2 (Prior Art)

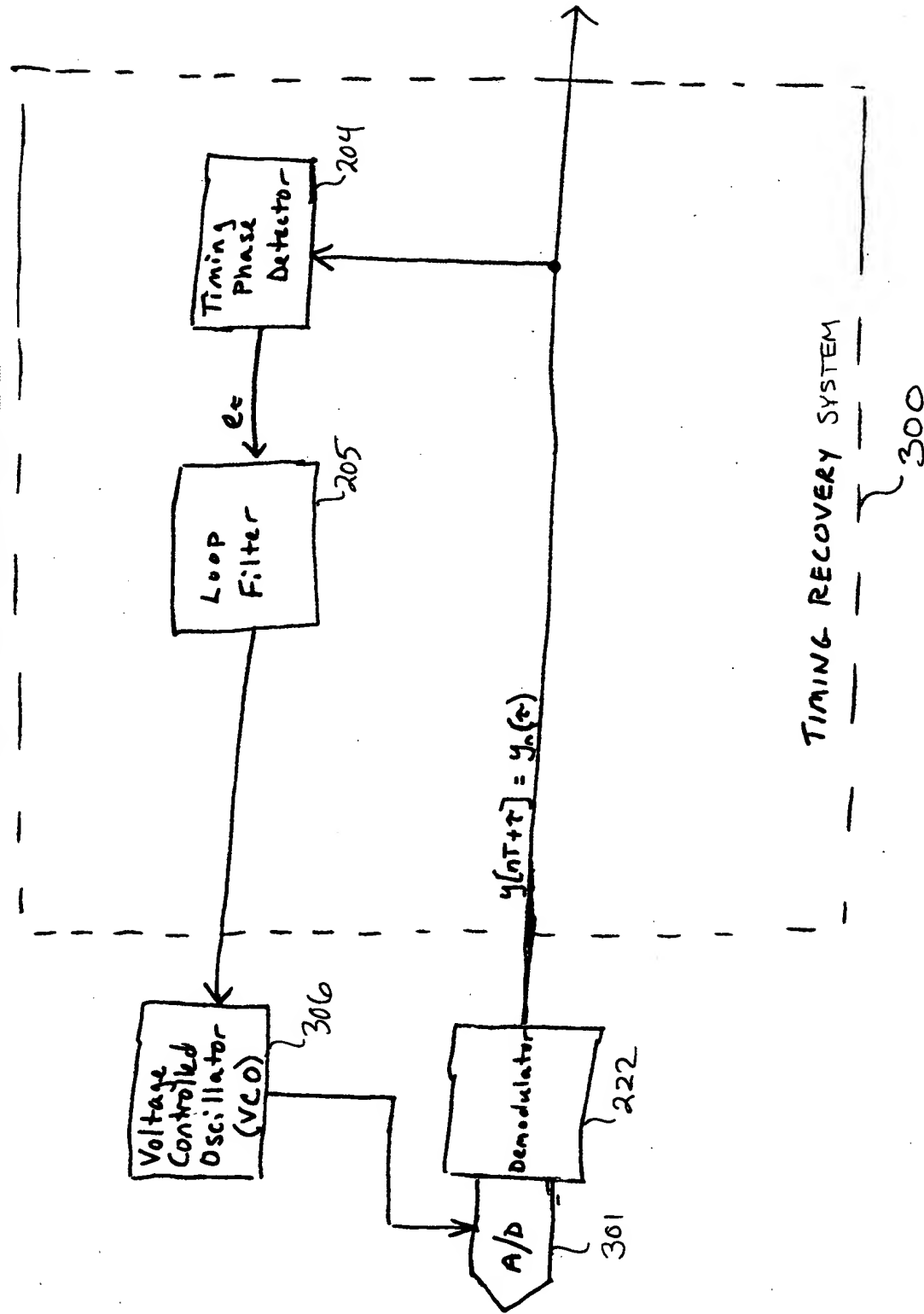
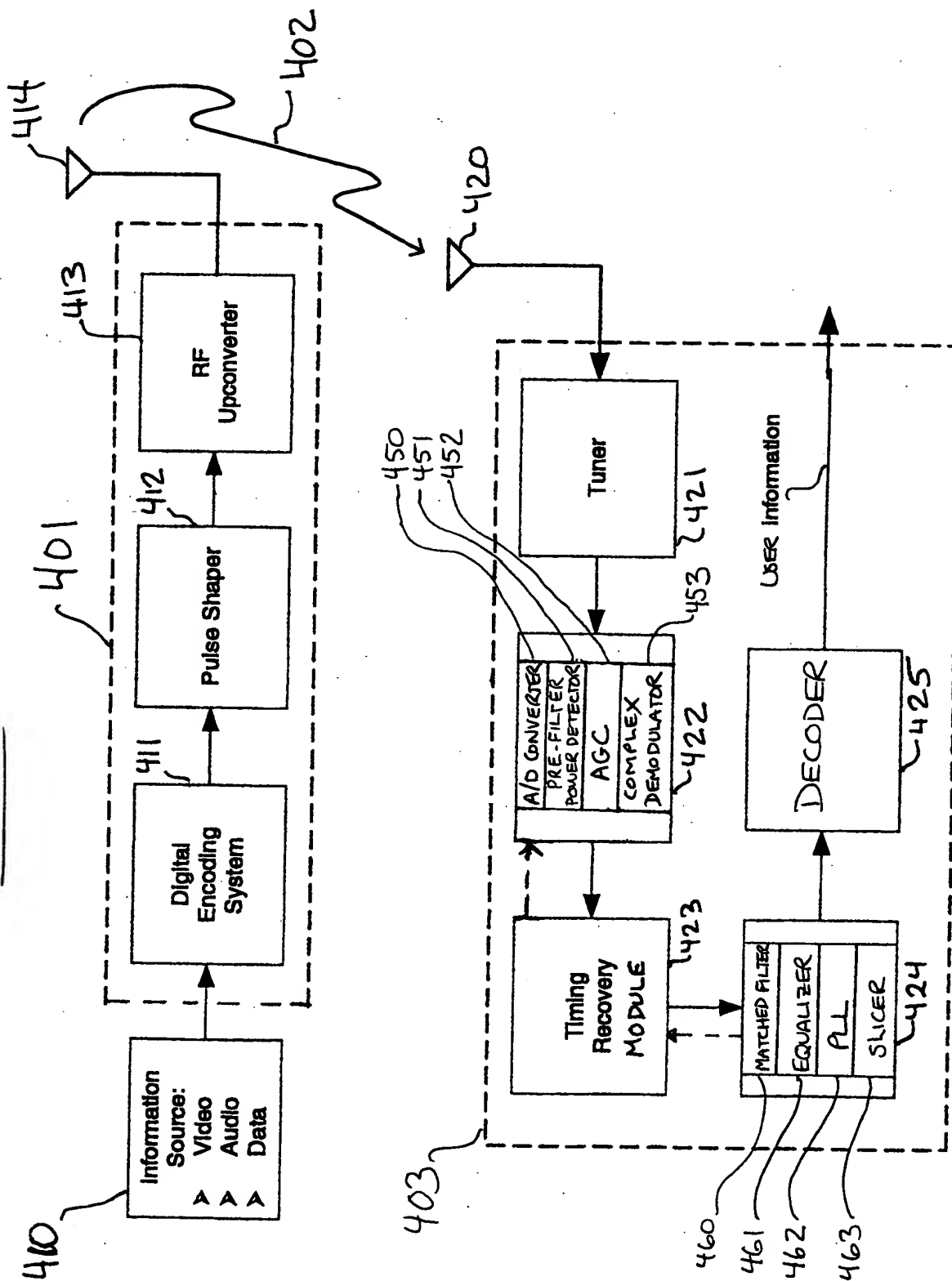


FIG. 3
(PRIOR ART)

FIG. 4

400



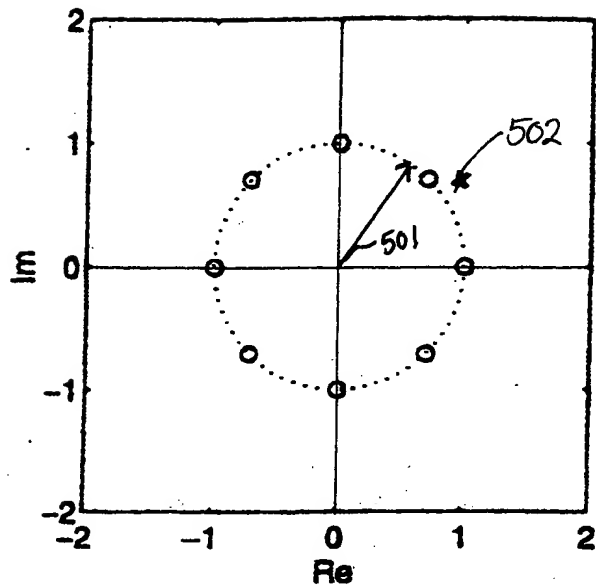


FIG. 5A

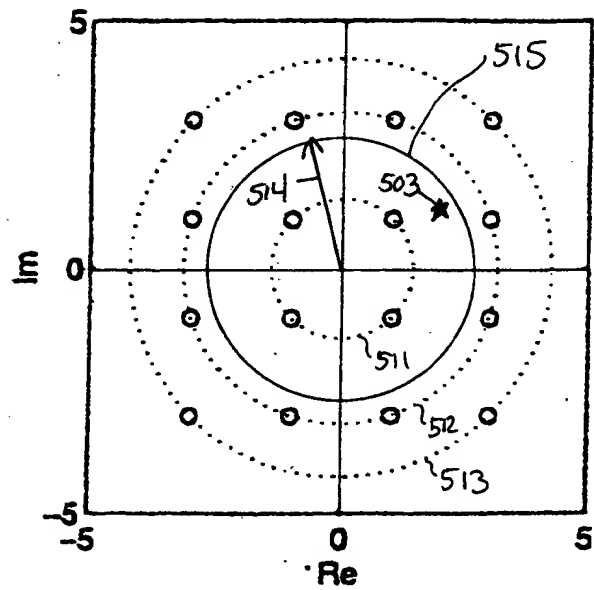


FIG. 5B

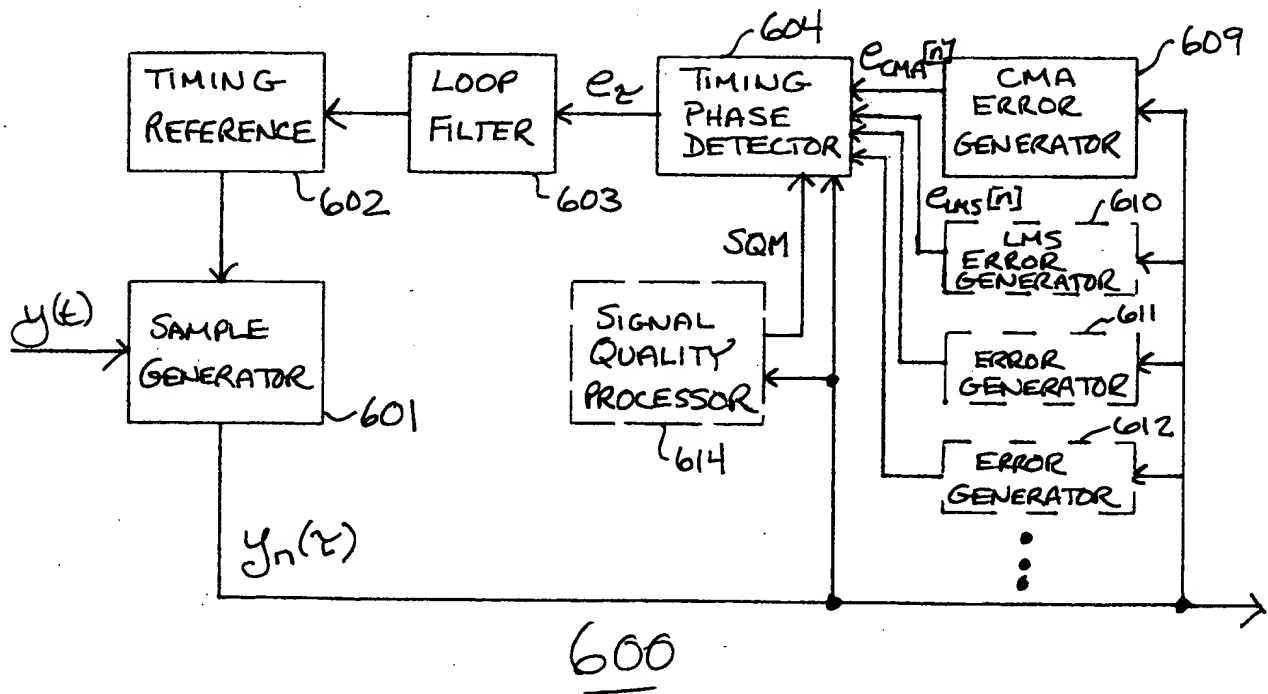
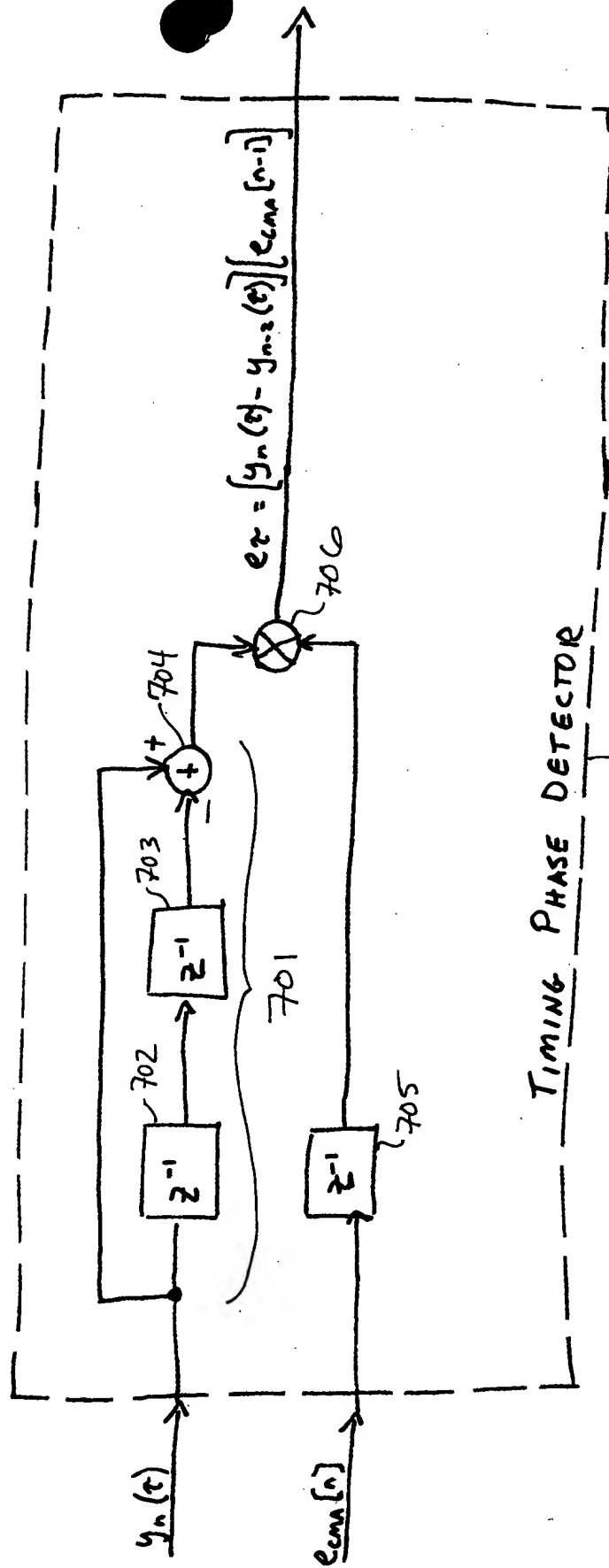


FIG. 6



TIMING PHASE DETECTOR

604

FIG. 7

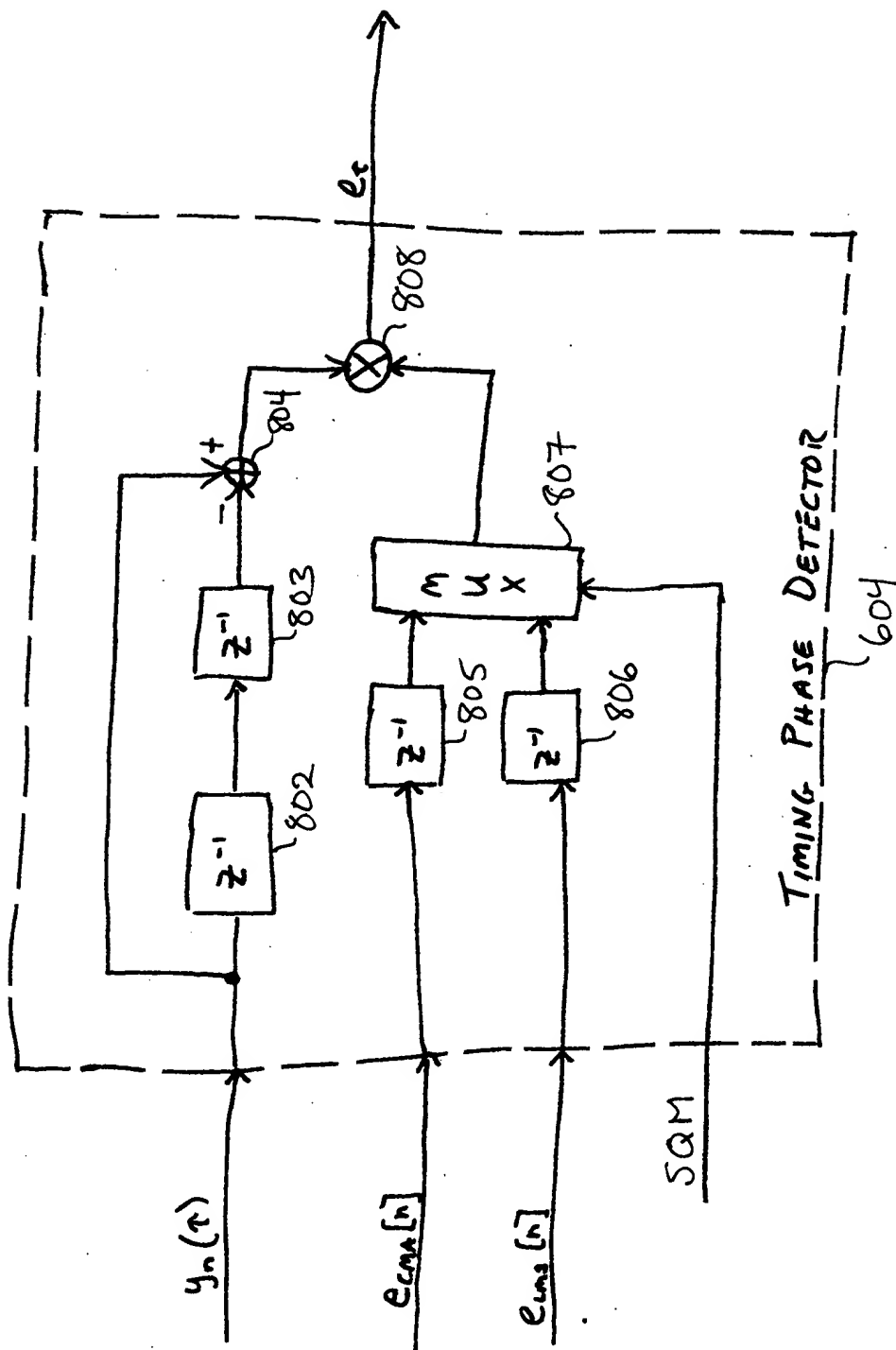


FIG. 8

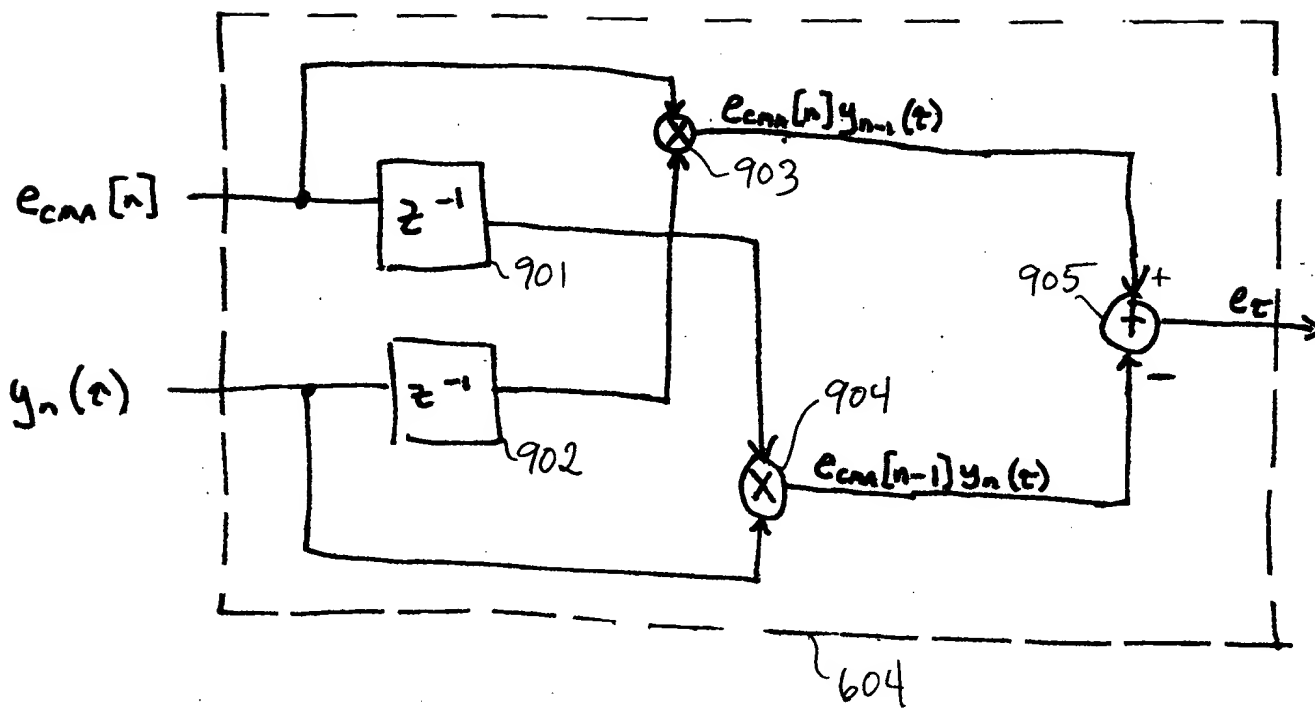


FIG. 9

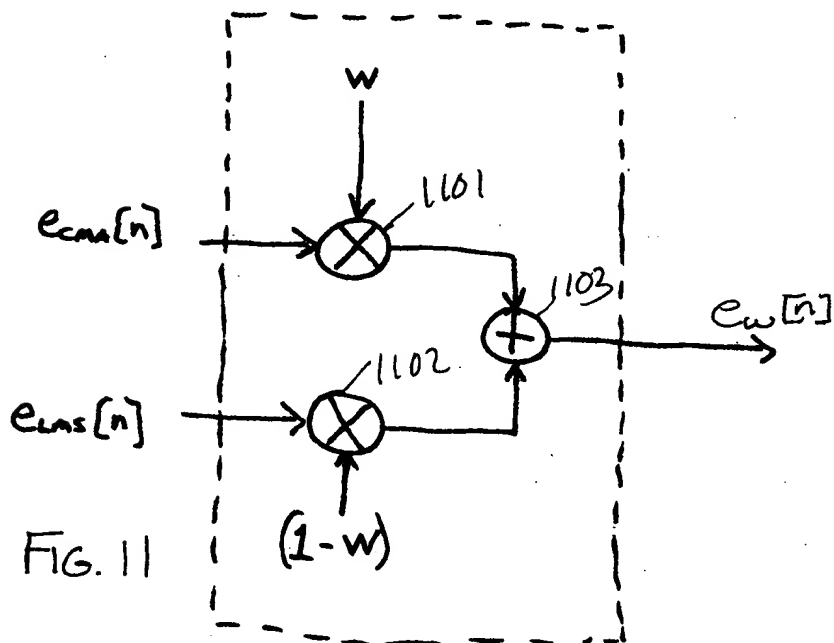


FIG. 11

When given, if you have any, in your own words, explain the block diagram of the timing phase detector.

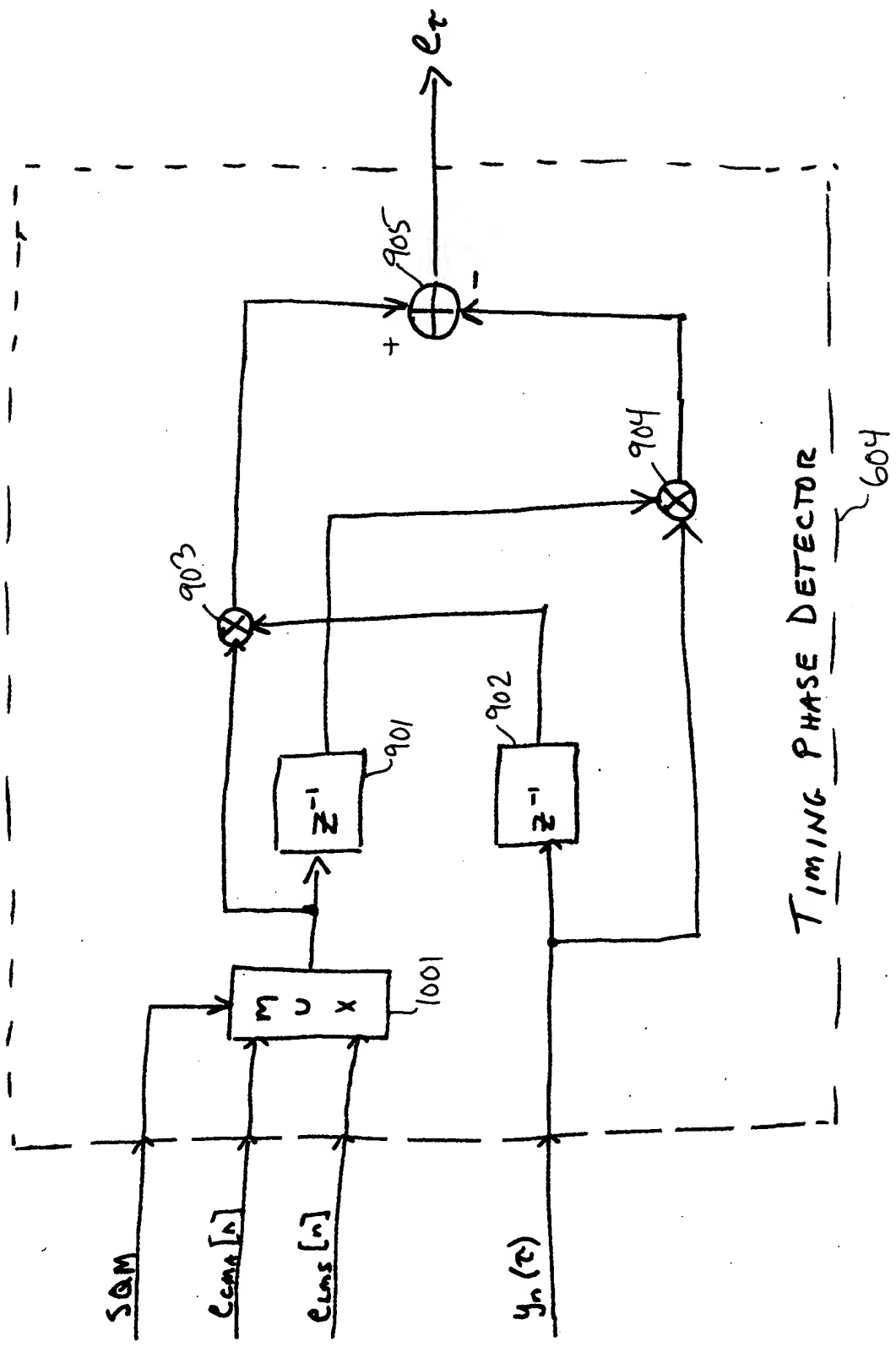


FIGURE 10

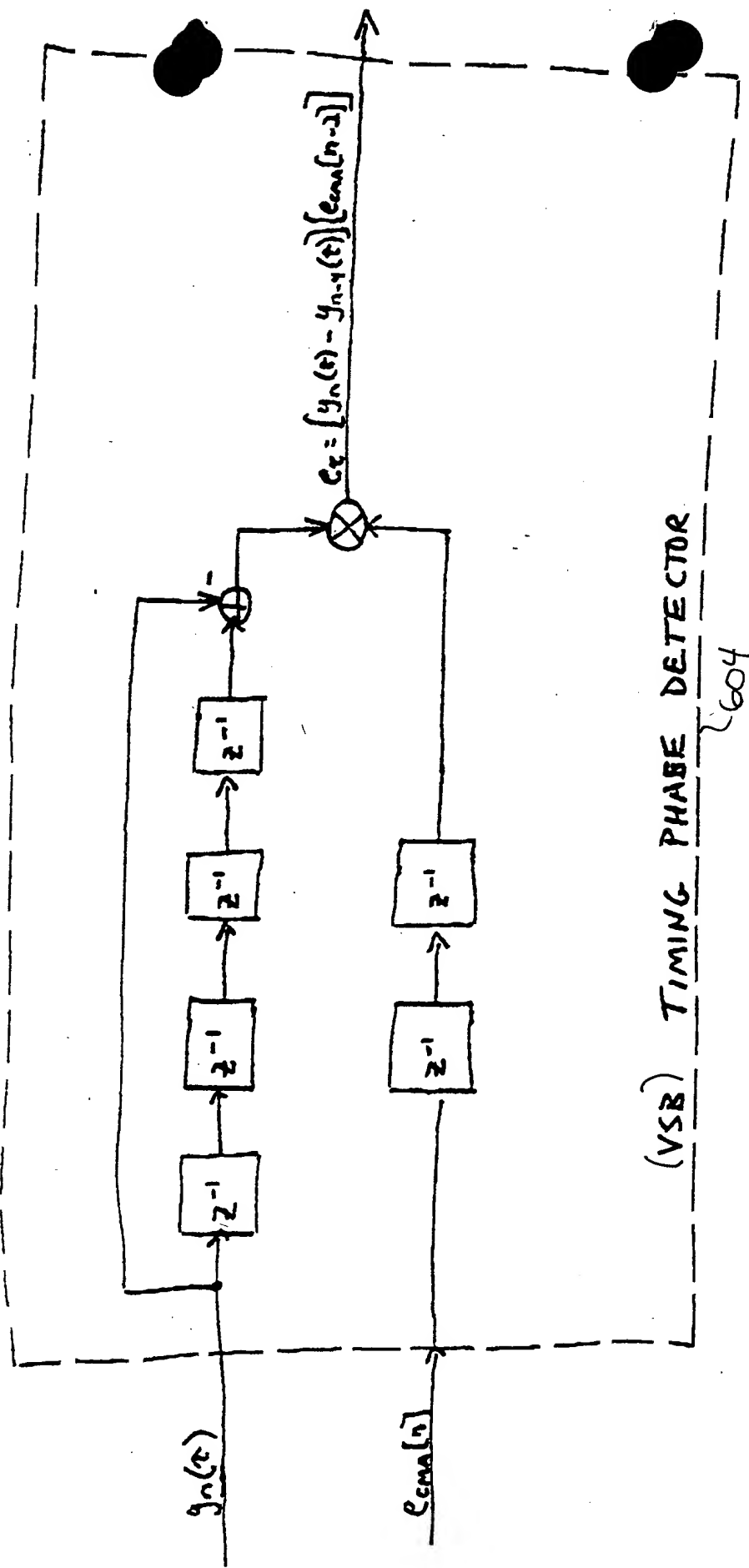


FIGURE 12

